

FIG. 1

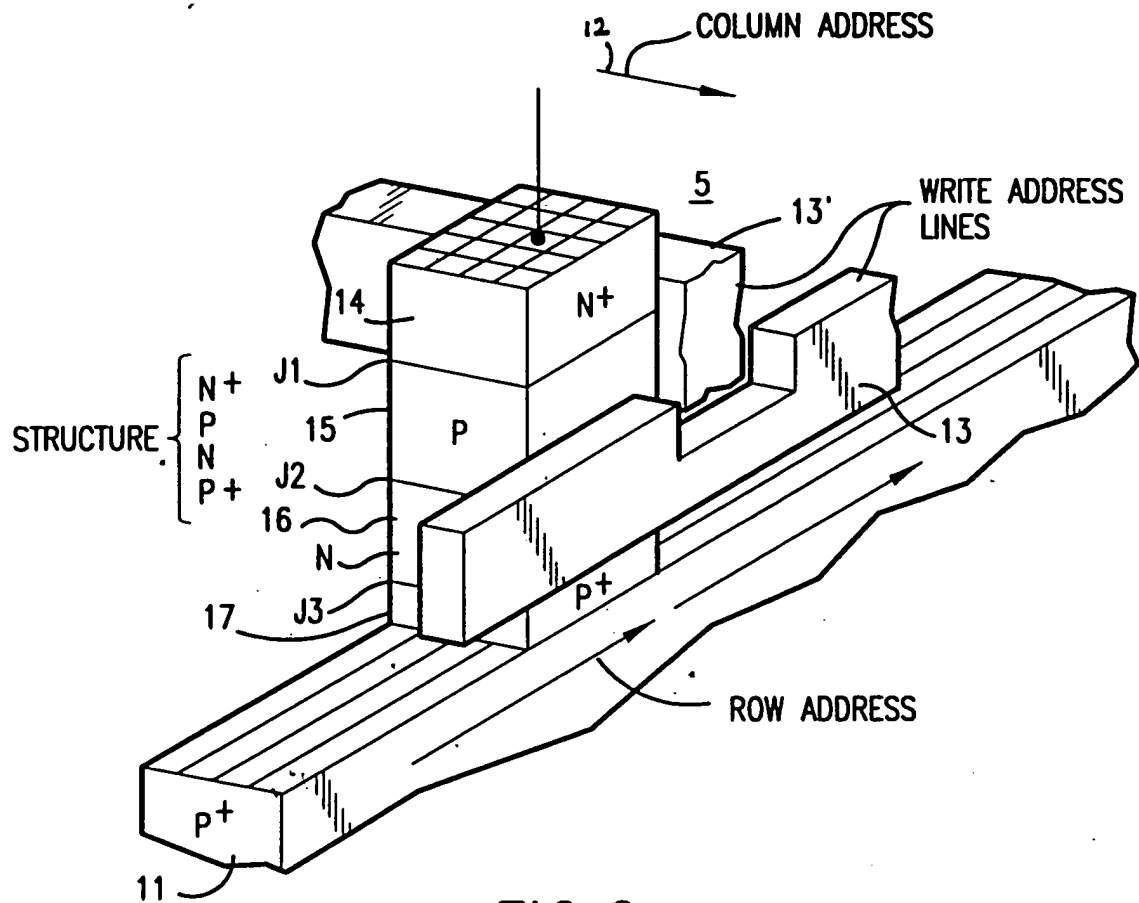


FIG.2a

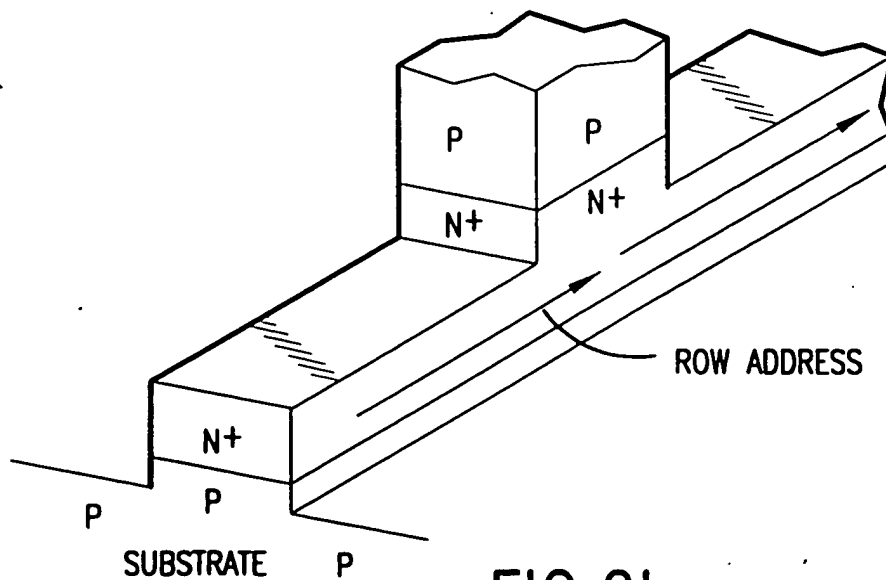
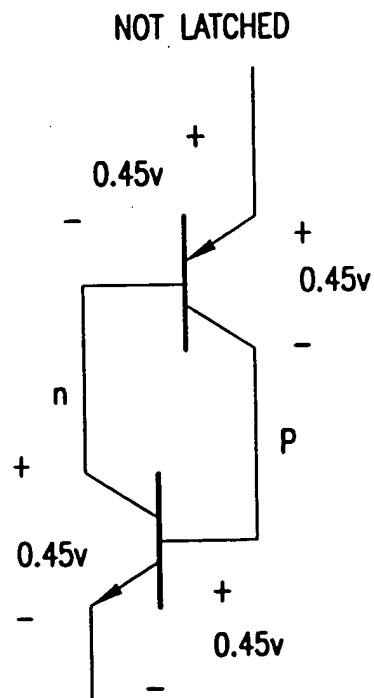
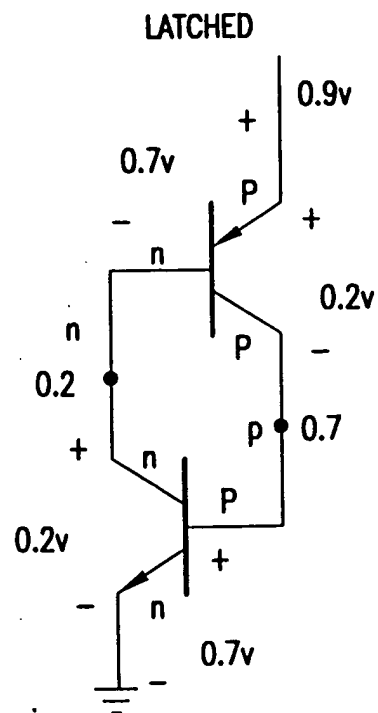


FIG.2b

The diagram illustrates a cross-sectional view of a semiconductor device. It features a central vertical channel labeled '5'. On the left side of the channel, there is a structure labeled '18' with an 'n' region at the bottom and a 'p' region at the top. A label 'J1' points to the 'n' region, and 'J2' points to the 'p' region. To the right of the channel, there is a structure labeled '19' with a 'p' region at the top and an 'n' region at the bottom. A label 'J3' points to the 'p' region, and '13'' points to the 'n' region. A label '13' points to a structure on the far left. The top of the device is labeled 'ROW ADDRESS' with a bracket, and the bottom is labeled 'COLUMN ADDRESS' with a bracket. The device is labeled '1T1C1R1B1M1'.

FIG. 3a



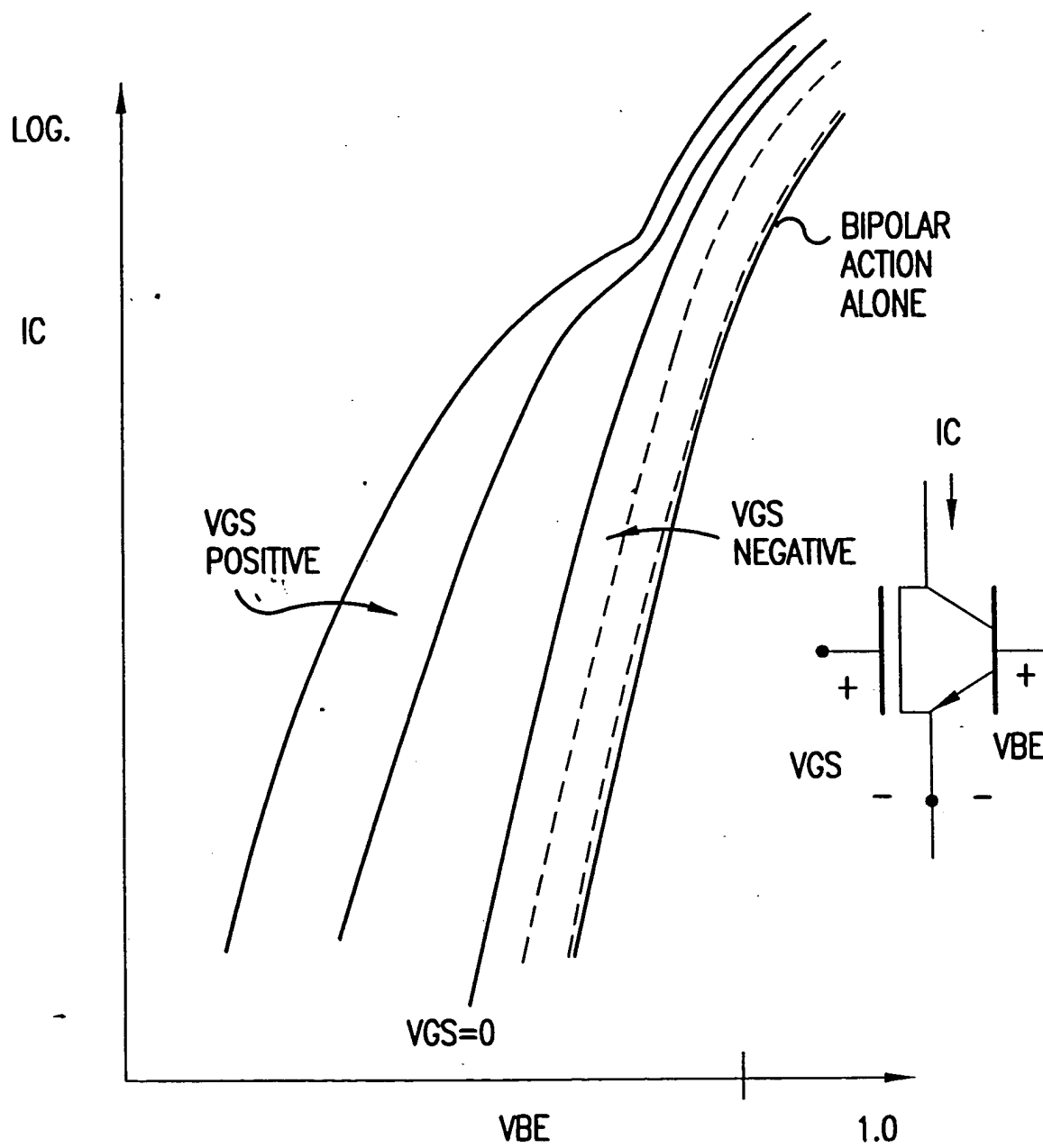


FIG.4

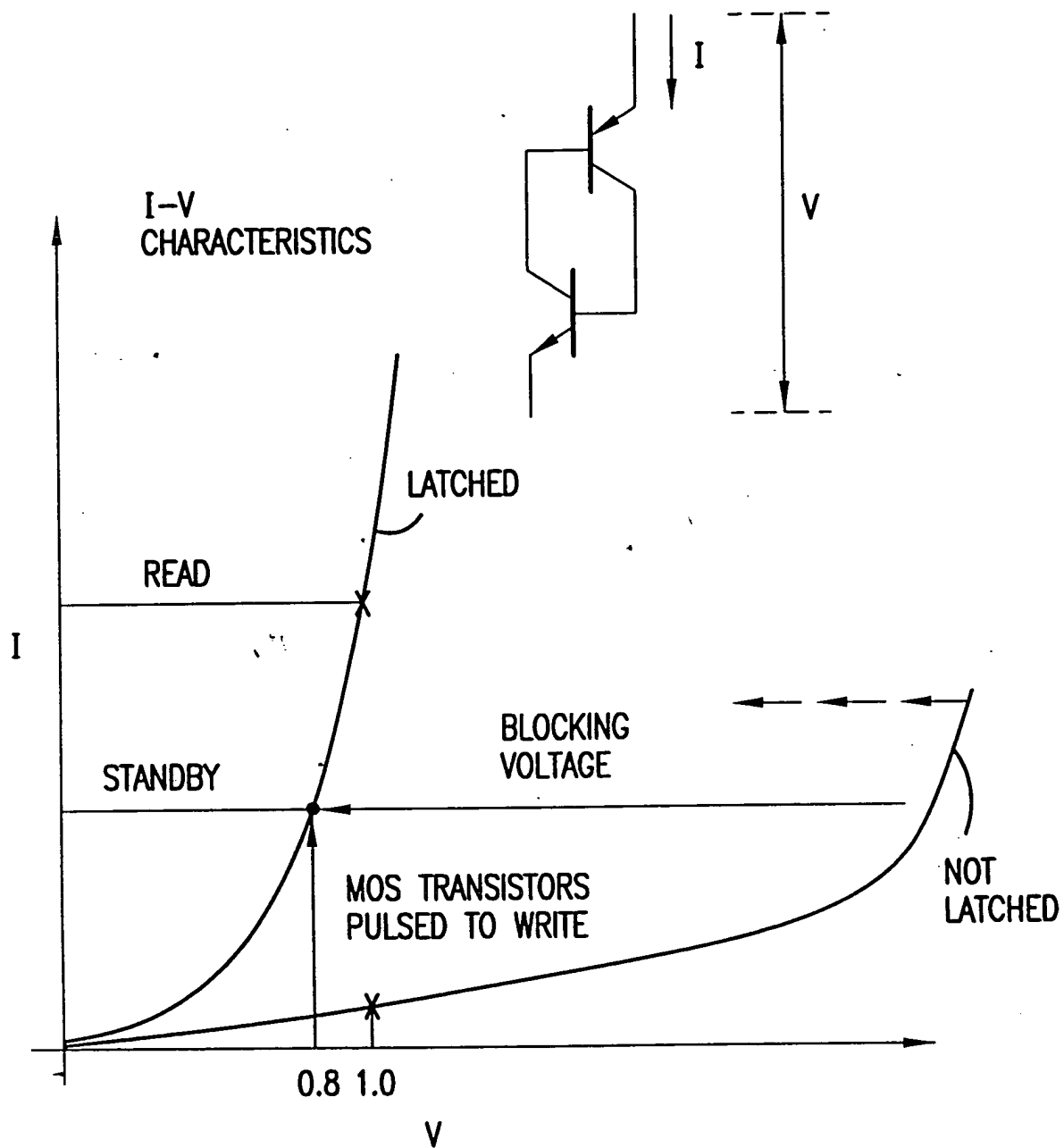


FIG.5

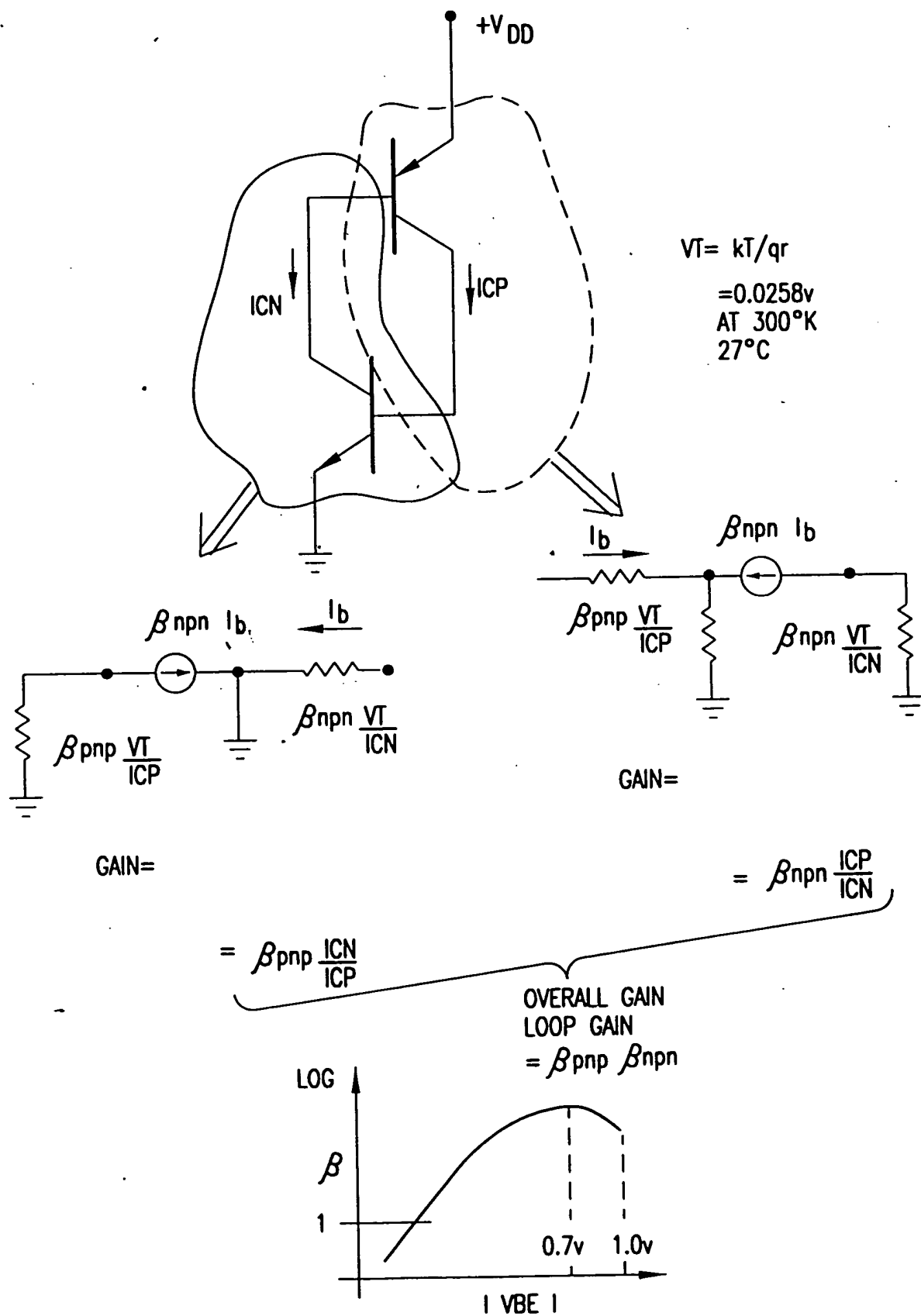


FIG.6

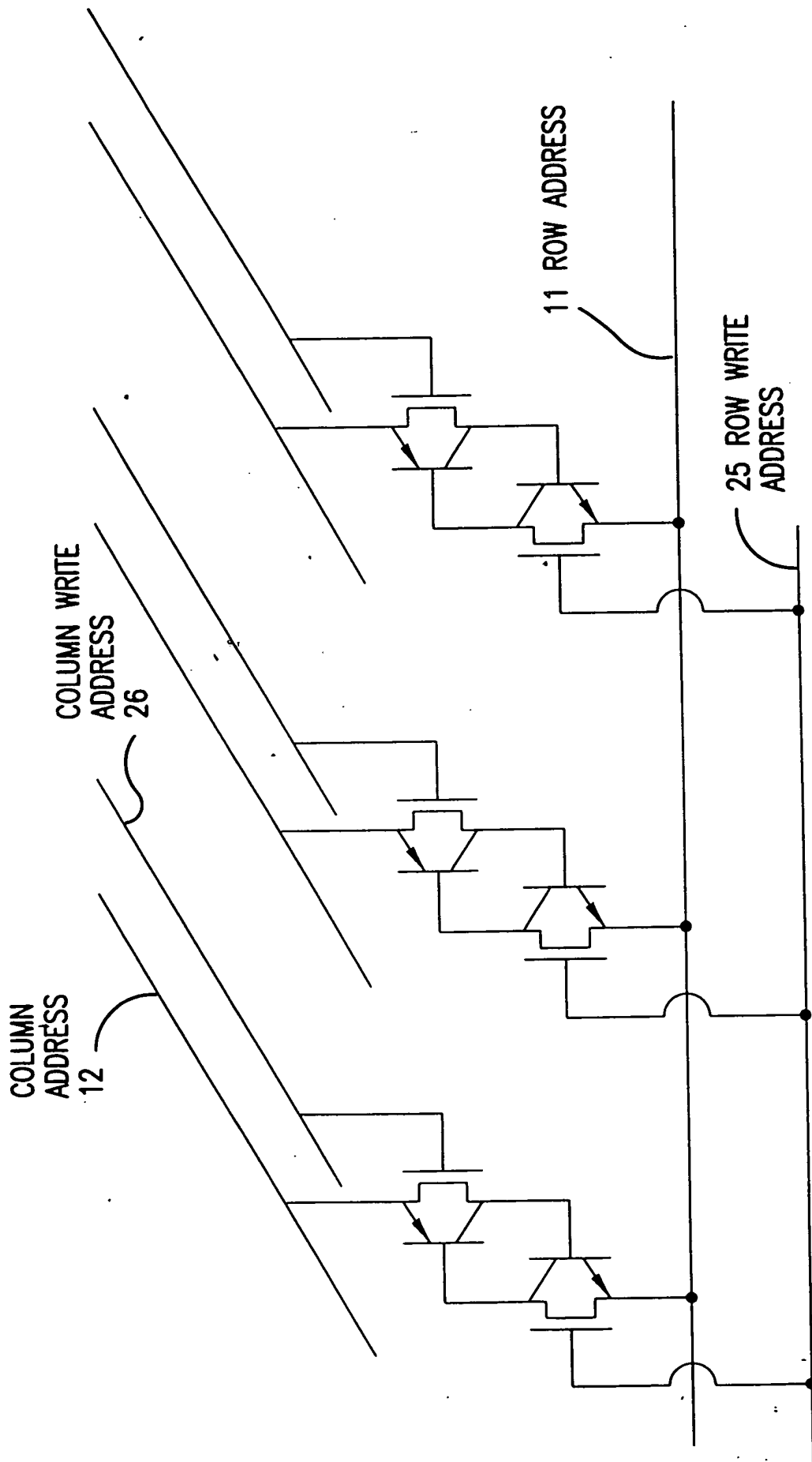


FIG.7

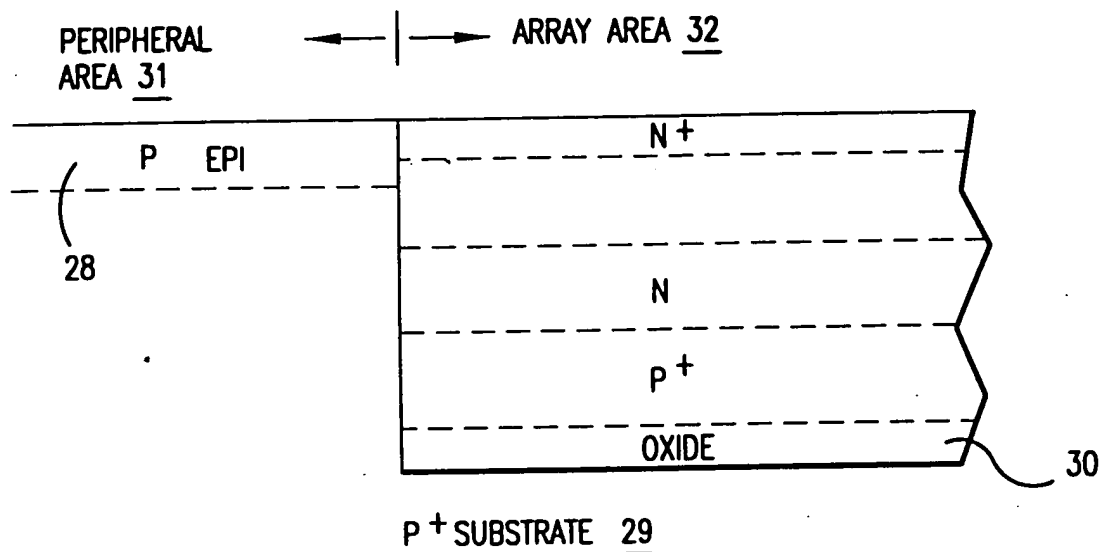


FIG.8

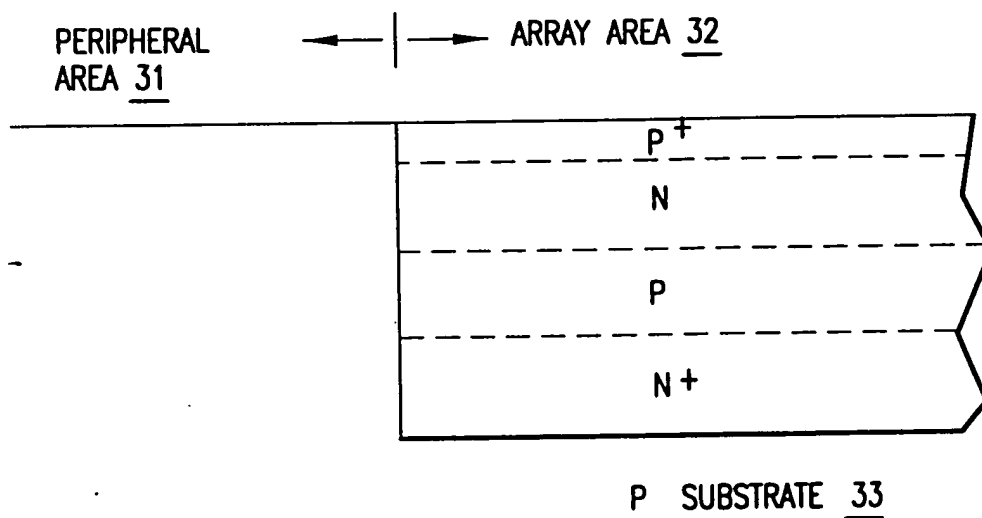


FIG.9

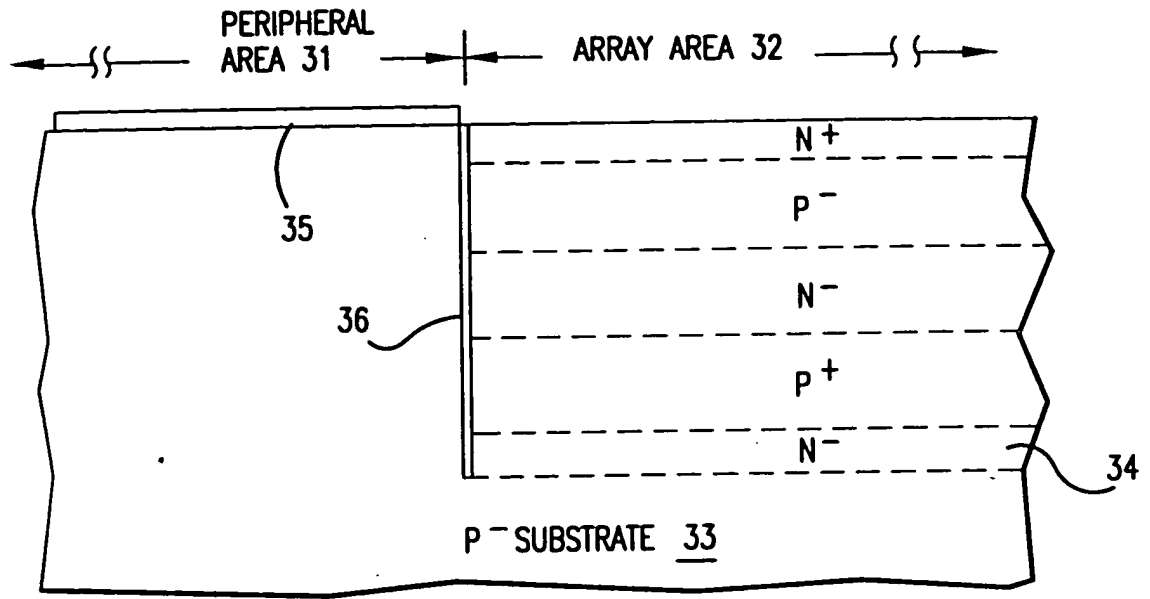


FIG.10

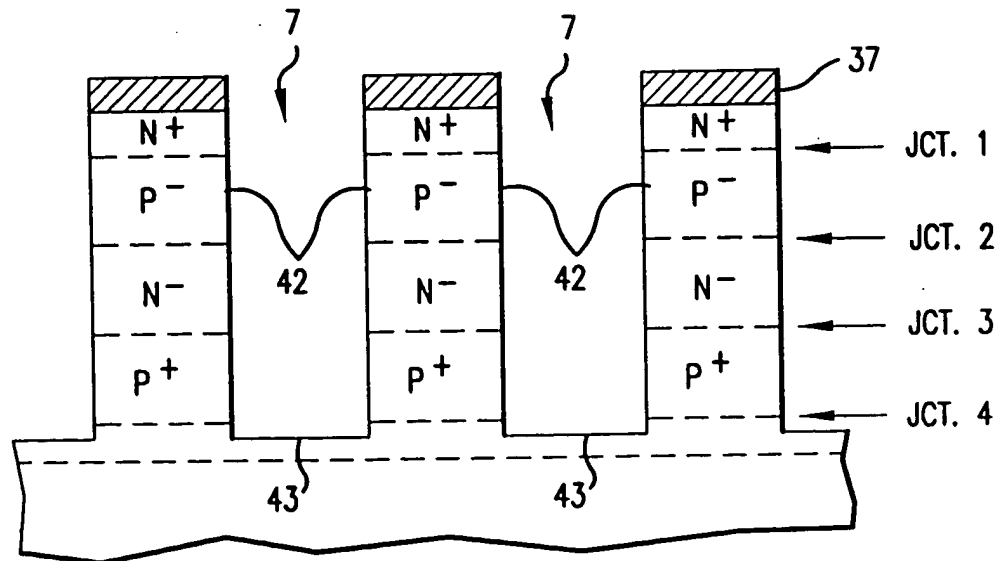


FIG.11

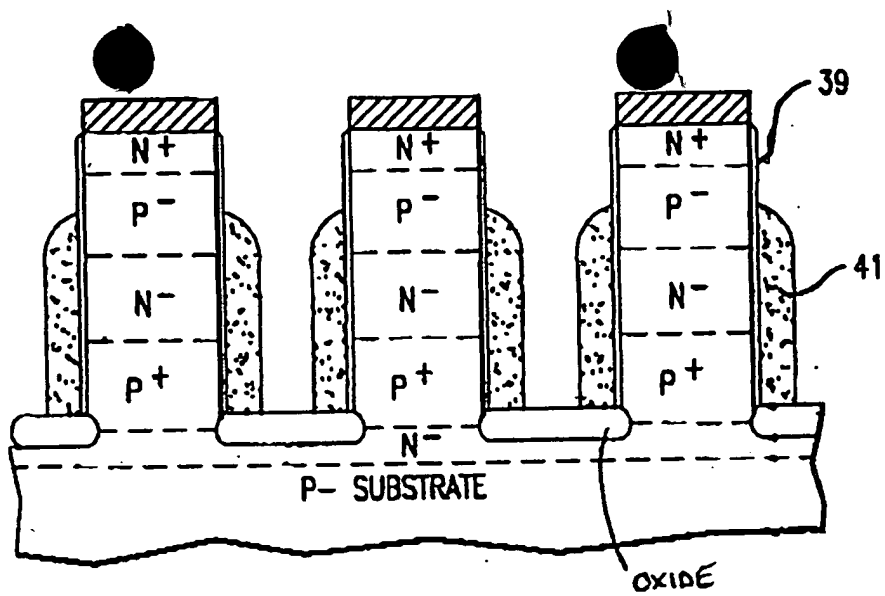


FIG.12

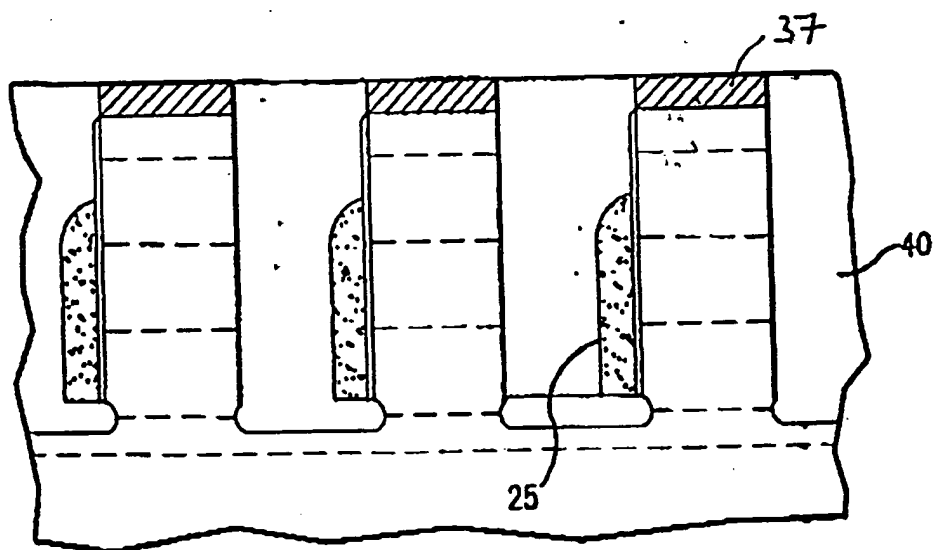


FIG.13

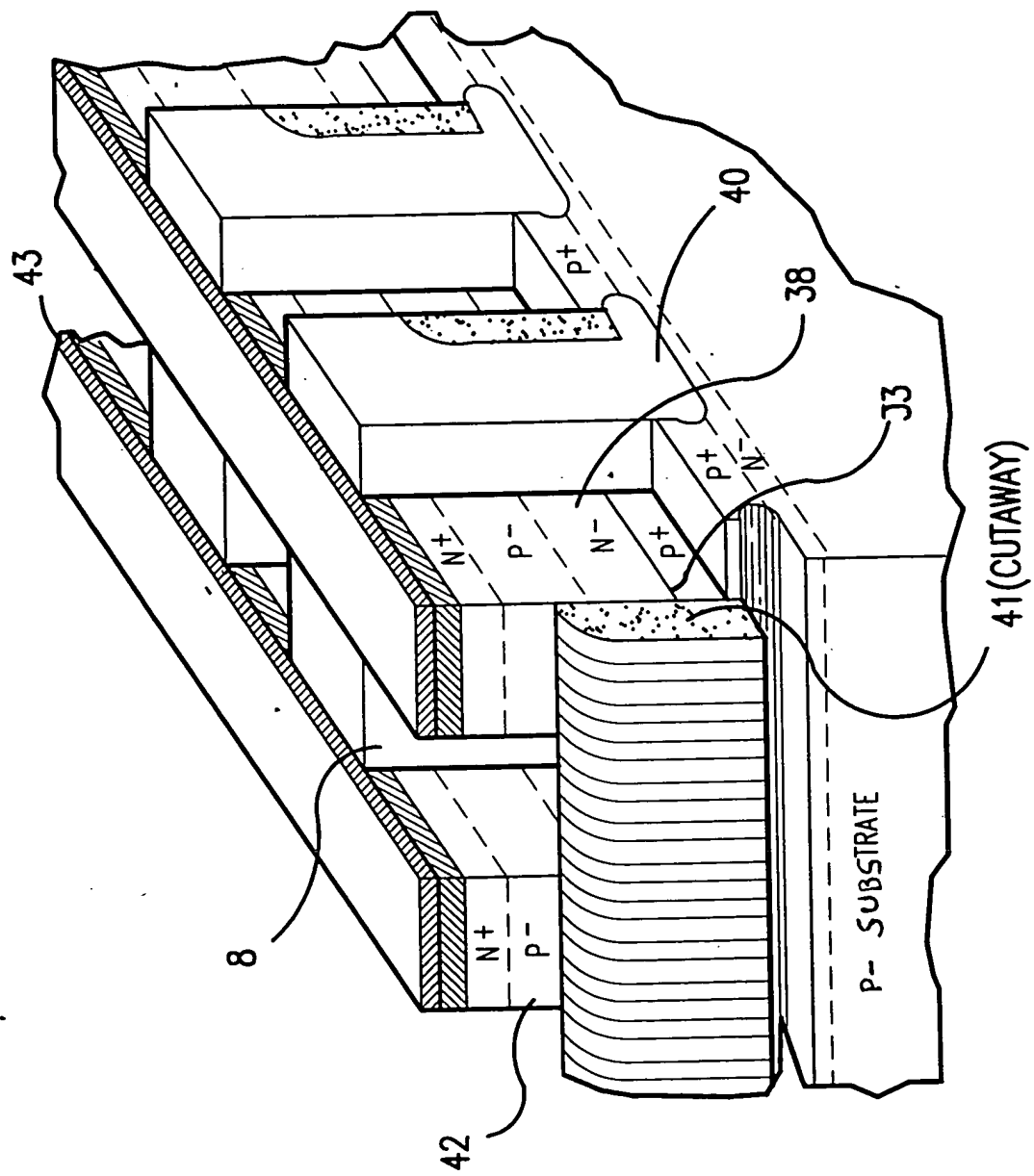


FIG. 15

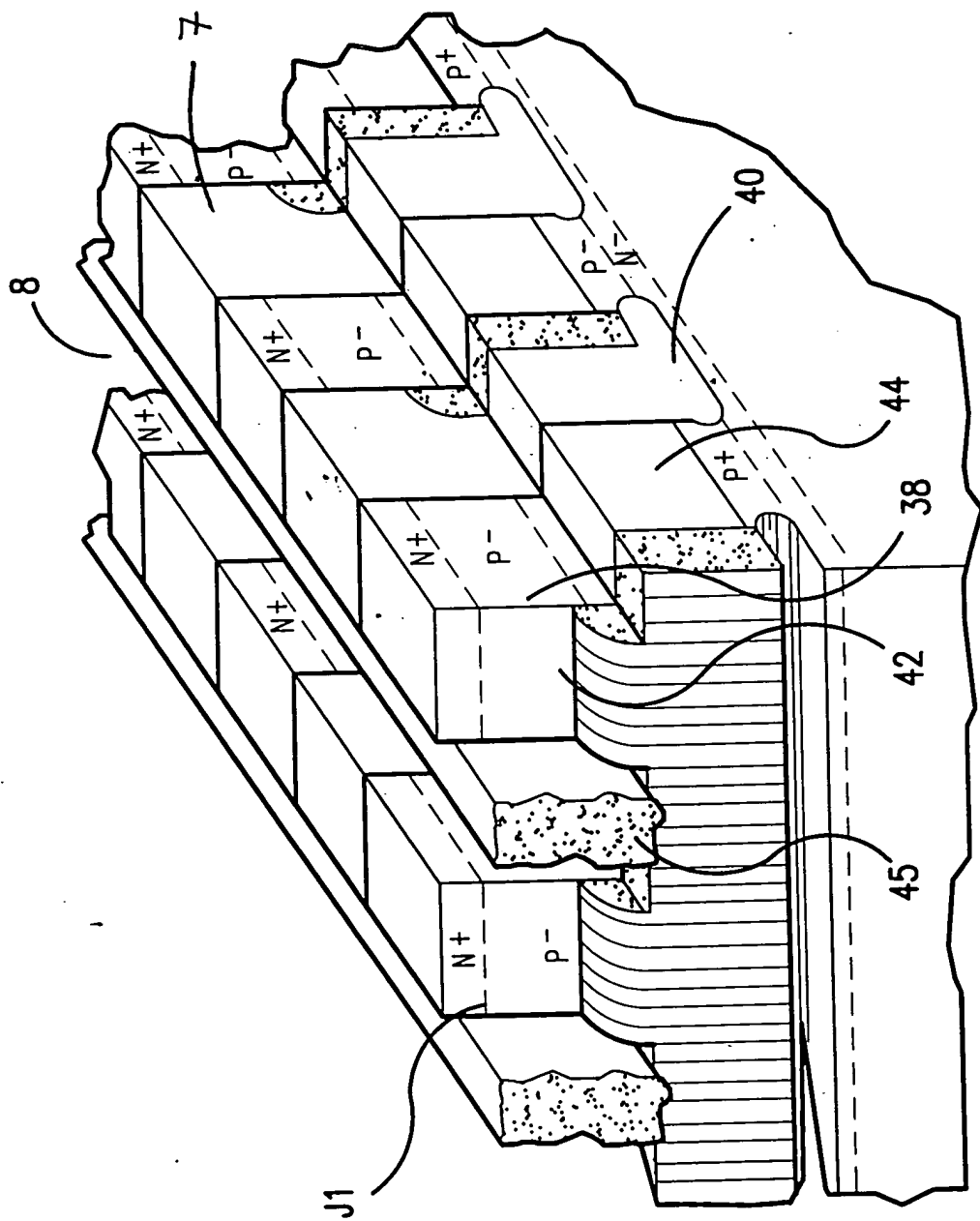


FIG. 16